

# SLM758 W •?ú rB\* D

9 U Æ 2018<sup>a</sup> 8 >

Ý ½, C0& Æ SLM758 W •?ú rB\* D

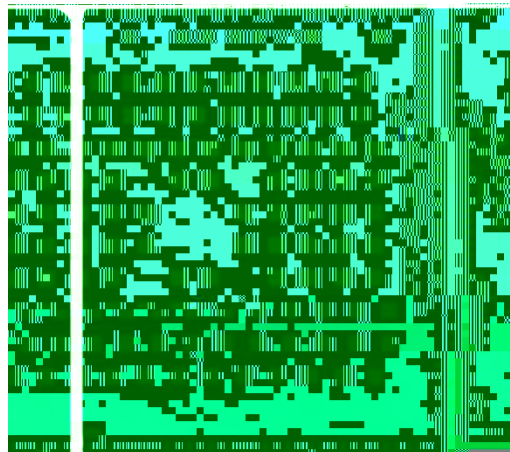
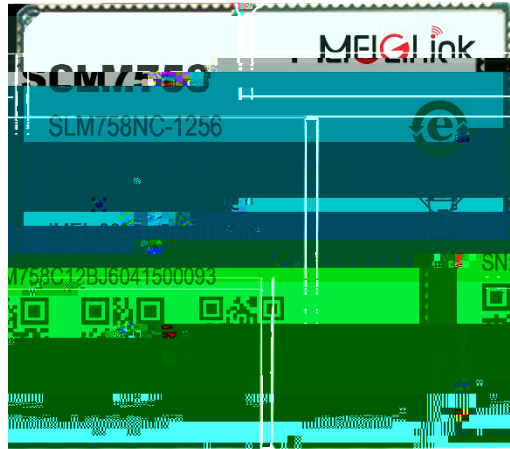
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SLM758 Q ‡?ô IB\$ > \_V1.02



# 1. x ñ ²F

SLM758 3+ G h ó ÷ ÈG ÷ +XQ FJP ±UÉ 00 3+ G, MSM8953 ÈB CPU G ÷ +Xl 4nm FinFET f @ È µ5ž 64bit ARM Æ 8 h Cortex A-53 Æ kNÁ 1.8Ghz/2.0Ghz/2.2Ghz 4\*6 ÷ È \_1 Decode/encode 0Q 4K 30fps Æ H.265 È \_1 ÷ µ ^ j 16GB/32GB/64GB, ÷ ~ \*3 = < f ? J Q LTE ÷ a 7- FJ Q ÷ È !” Q ÷ F2 +X ¾ TD-LTE/FDD-LTE/WCDMA/EVDO/TD-SCDMA/CDMA/GSM J/ý5•5 f?, í V a7- 4iFJ Q ÷ Æ SLM758 G ÷ +X Android 7.1 ý Æ 3+5 È \_1, ÷ Ö •FO) • Ö

TD-LTE: 117/30Mbps

FDD-LTE: 150Mbps/50Mbps

WCDMA Eî DC HSPA+: 42Mbps/5.76Mbps

EVDO Eî EVDO RevA: 3.1Mbps/1.8Mbps

TD-SCDMA Eî HSPA: 4.2Mbps/2.2Mbps

CDMA1x: 153.6kbps/153.6kbps

GSM Eî EDGE: 236.8kbps/236.8kbps

SLM758 X ÈQ FO í V ž Ö •, < & È ÈB N# Æ. Æ FJA Æ 2o Æ 2.4G/5G WiFi Æ BT ¼ GPS İ7- x x ñ \_1 ü 1300W, ÷ 3D t ý F Ý \$! ý — È ÷ # Æ +X ¾ — # Æ A— Æ FJ Æ POS fK & j Æ (™ # q4ø1 Æ VR Camera Æ a7- j ~ ê Æ ?öNÁ- x Æ 'Lb Æ E—E—Aî 7 Æ a 7- { 1 4ø1 1y x ñ Æ

## 2. (© W G>~

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	LW/G/T+G LW/G/T+W LW/G/T+1X L/EVDO/CDMA1X+G = _ 1 ü CDMA '	
Display	Matrix:	
	FULL HD: 1920*1200 60fps;	
	LCM Size: User defined	
	Interface: 1st LCM ÖMIPI DSI 4-lane; 2nd LCM ÖMIPI DSI 4-lane	
Camera Ä } ¼ > ü t ÿ d Ä	Interface: _ 1 94ö CSI Èÿ 04öG- _ 4-Lane	
	t ÿ d Pixel: >5ž 13-24Mp/ )5ž up to 13Mp È ü ISP < & _ 1 ü 13MP Camera	
	Video decode	4K 30fps Ä SLM758L = _ 1 Ä, 1080p 60fps, H.264/H.265 MP4/ WMV9/ VC1/ DivX/ VP8/VP9
Video encode	4K 30fps Ä SLM758L = _ 1 Ä, 1080p 60 fps: H.264/ H.265/ MP4/ VP8 È 1080P 60fps De +1080p 30fps En Ä SLM758L = _ 1 Ä	
EÄ •Äî 7	9K^ Ä 0 £ jK^ Ä home ÄN#Gÿ ÄN#GÿÄ	
	+e é ? TP	
Reset	_ 1.œ = }	
	Ö =0	k?± ĩ7- ÿF
	VBAT	3pin

Ä+X Ö

		5G WiFi- Y4i
	Audio	1 D k MIC 1 D L} š MIC 1 D } · Ä µ5ž 0.8W D 2« ĩ n Ä 1 D \1, 1 D 0û f 6c j

### 3. ĩ f ?(x \

5•5 NÁ!â Ä Ê!W(x \ Ä SLM758-E	TDD-LTE: B38/40/41 FDD-LTE: B1/3/5/7/8/20 WCDMA: B1/B5/B8 GSM: B5/3/8
5•5 NÁ!â Ä 5¼#b(x \ Ä SLM758-A	FDD-LTE ÖB2/4/5/7/12/13/17/28b WCDMA ÖB2/4/5
5•5 NÁ!â Ä \ (x \ Ä SLM758-J	FDD-LTE ÖB1/3/8/9/19/26/28 WCDMA ÖB1/8/9/19

## 4.SLM758 Ö

### SLM758 Q ‡ 272pin Ê y Ö

PIN#	SLM758 Pin name	Pad characteristics	Functional description
1	GND	GND	GND
2	FLASH_LED1+	AO	FLASH LED anode(1A)
3	GND	GND	GND
4	PMI8952_MPP1	AO-Z,AI,DO	Configurable MPP,PWM,ADC
5	LCD_BL_LED_K1	AI	LCD Backlight cathode1(20mA)
6	LCD_BL_LED_K2	AI	LCD Backlight cathode2(20mA)
7	LCD_BL_LED_A	PO	LCD Backlight anode
8	PMI_HAP_OUT_N	AO	Haptics driver output negative
9	PMI_HAP_OUT_P	AO	Haptics driver output positive
10	GND	GND	GND
11	UART2_MSM_TX	B-PD:nppukp	Configurable I/O,UART2 TX
12	UART2_MSM_RX	B-PD:nppukp	Configurable I/O,UART2 RX
13	GPIO6_I2C2_SDA	B-PD:nppukp	Configurable I/O, I2C SDA
14	GPIO7_I2C2_SCL	B-PD:nppukp	Configurable I/O, I2C SCL
15	GPIO12_UART4_TX	B-PD:nppukp	Configurable I/O,UART4 TX
16	GPIO13_UART4_RX	B-PD:nppukp	Configurable I/O,UART4 RX
17	GPIO14_SENSOR_I2C4_SDA	B-PD:nppukp	Configurable I/O,SENSOR I2C SDA
18	GPIO15_SENSOR_I2C4_SCL	B-PD:nppukp	Configurable I/O, SENSOR I2C SCL
19	GPIO42_ACCL_INT1	B-PD:nppukp	Configurable I/O,ACC INT
20	GPIO43_ALSP_INT_N	B-PD:nppukp	Configurable I/O,ALSP INT
21	GPIO44_MAG_INT	B-PD:nppukp	Configurable I/O,MAG INT
22	GPIO45_GYRO_INT	B-PD:nppukp	Configurable I/O,GYRO INT
23	GND	GND	GND
24	GPIO85_KEY1_VOL+	B-PD:nppukp	Configurable I/O,KEY VOL+
25	GPIO86_KEY2_SNAPSHOT	B-PD:nppukp	Configurable I/O,KEY VOL- or SNAPSHOT



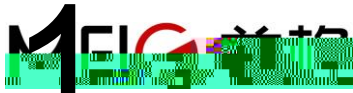
			MI2S1_D1
28	GND	GND	GND
29	VREG_L11_2P95	PO	PMIC output 2.95V for SD-card power
30	VREG_L12_PX2_2P95	PO	PMIC output 2.95V for SDC2 signal
31	GPIO133_SDCARD_DET_N	B-PD:nppukp	Configurable I/O,SD card detection
32	SDC2_SDCARD_D0	BH-NP:pdpukp	Secure digital controller 2 data bit 0
33	SDC2_SDCARD_D1	BH-NP:pdpukp	Secure digital controller 2 data bit 1





95	CAM_I2C_SCL1	B-PD:nppukp	Configurable I/O, Dedicated camera I2C1 SCL
96	CAM_I2C_SDA1	B-PD:nppukp	Configurable I/O, Dedicated camera I2C1 SDA
97	GPIO65_TP_INT_N	B-PD:nppukp	Configurable I/O,TP INT
98	GPIO64_TP_RST_N	B-PD:nppukp	Configurable I/O,TP RESET
99	GPIO63	B-PD:nppukp	Configurable I/O,
100	GPIO59	B-PD:nppukp	Configurable I/O,
101	GND	GND	GND
102	DIV_ANT	AI	RF signal for diversity ANT
103	GND	GND	GND
104	CAM_I2C_SCL0	B-PD:nppukp	Configurable I/O, Dedicated camera I2C0 SCL
105	CAM_I2C_SDA0	B-PD:nppukp	Configurable I/O, Dedicated camera I2C0 SDA
106	GPIO54_UIM1_DET	B-PD:nppukp	Configurable I/O,UIM1 removal detection
107	UIM1_RESET	B-PD:nppukp	Configurable I/O,UIM1 reset
108	UIM1_CLK	B-PD:nppukp	Configurable I/O,UIM1 clock
109	UIM1_DATA	B-PD:nppukp	Configurable I/O,UIM1 data
110	VREG_L14_UIM1	PO	





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159	VBUS	PI,PO	USB Voltage
160	GND	GND	GND



193	MIPI_CSI0_LANE1_P	AI	MIPI camera serial interface 0 lane1+
194	MIPI_CSI0_LANE1_N	AI	MIPI camera serial interface 0 lane1-

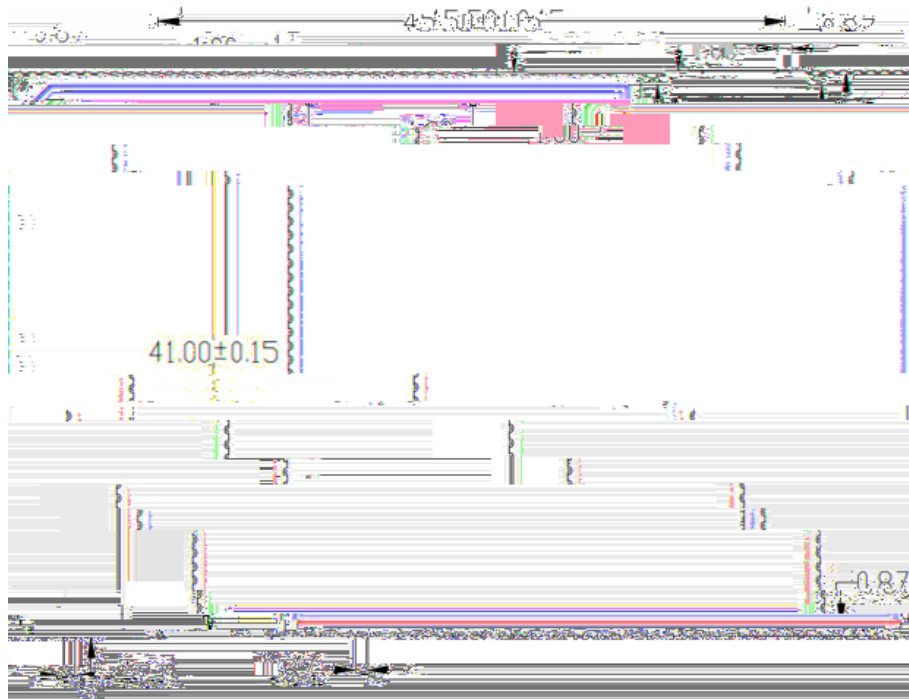


224	GND	GND	GND
225	MIPI_DS11_LANE0_P	AO	MIPI display serial interface 1 lane0+



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TOP Ö



BOTTOM:

Side:

